

ABSTRACT OF THE DISCLOSURE

A signal strength indicator circuit including a set of rectifiers. The set of rectifiers include an asymmetric switching pair of rectifiers that may be configured to provide an output signal that is inversely proportional to an input signal obtained from a device in a telecommunications system. The pair may be connected directly on one side thereof to a power supply and may be connected on another side thereof to the power supply through a resistor. Also, a method of processing a signal received by a signal strength indicator circuit.